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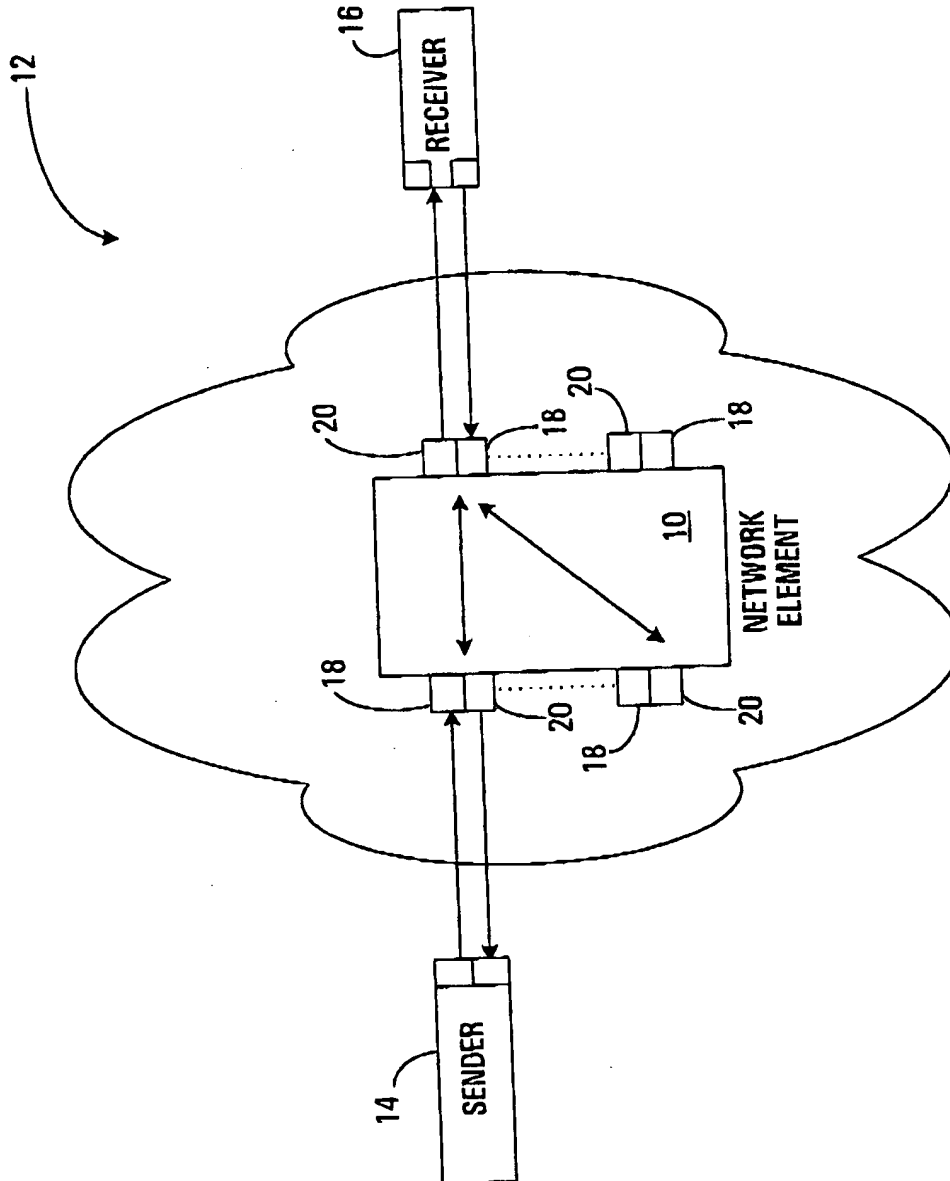


FIGURE 1

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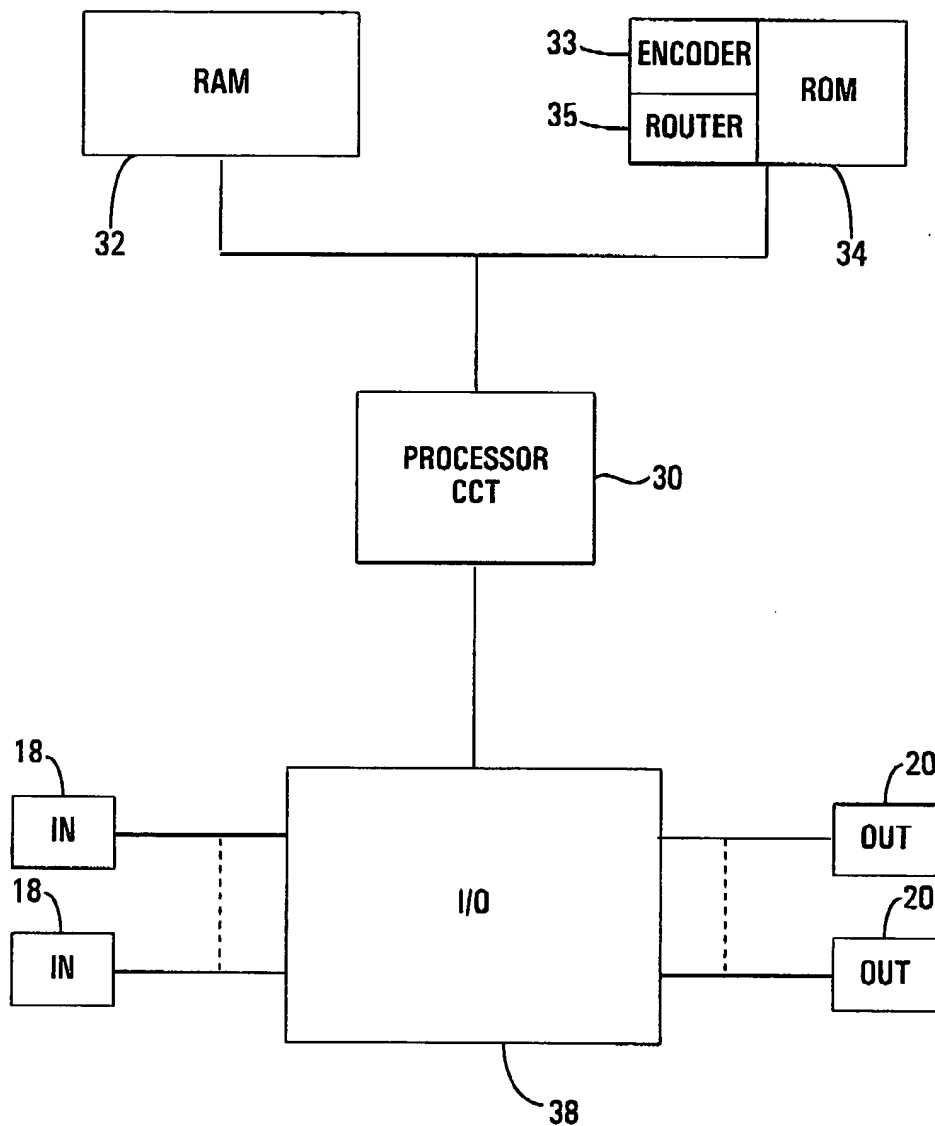


FIGURE 2

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ROW	LISTED PREFIX	PREFIX LENGTH	NEXT HOP INFO
1	0	1	nh <sub>1</sub>
2	00	2	nh <sub>2</sub>
3	0010	4	nh <sub>3</sub>
4	01	2	nh <sub>4</sub>
5	010	3	nh <sub>5</sub>
6	011	3	nh <sub>6</sub>
7	1	1	nh <sub>7</sub>
8	11	2	nh <sub>8</sub>
9	1110	4	nh <sub>9</sub>
10	1111	5	nh <sub>10</sub>

42

44 46 48

FIGURE 3

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1)	0	-	-	-	-	32)	0	0	0	0	1
2)	1	-	-	-	-	33)	0	0	0	1	0
3)	0	0	-	-	-	34)	0	0	0	1	1
4)	0	1	-	-	-	35)	0	0	1	0	0
5)	1	0	-	-	-	36)	0	0	1	0	1
6)	1	1	-	-	-	37)	0	0	1	1	0
7)	0	0	0	-	-	38)	0	0	1	1	1
8)	0	0	1	-	-	39)	0	1	0	0	0
9)	0	1	0	-	-	40)	0	1	0	0	1
10)	0	1	1	-	-	41)	0	1	0	1	0
11)	1	0	0	-	-	42)	0	1	0	1	1
12)	1	0	1	-	-	43)	0	1	1	0	0
13)	1	1	0	-	-	44)	0	1	1	0	1
14)	1	1	1	-	-	45)	0	1	1	1	0
15)	0	0	0	0	-	46)	0	1	1	1	1
16)	0	0	0	1	-	47)	1	0	0	0	0
17)	0	0	1	0	-	48)	1	0	0	0	1
18)	0	0	1	1	-	49)	1	0	0	1	0
19)	0	1	0	0	-	50)	1	0	0	1	1
20)	0	1	0	1	-	51)	1	0	1	0	0
21)	0	1	1	0	-	52)	1	0	1	0	1
22)	0	1	1	1	-	53)	1	0	1	1	0
23)	1	0	0	0	-	54)	1	0	1	1	1
24)	1	0	0	1	-	55)	1	1	0	0	0
25)	1	0	1	0	-	56)	1	1	0	0	1
26)	1	0	1	1	-	57)	1	1	0	1	0
27)	1	1	0	0	-	58)	1	1	0	1	1
28)	1	1	0	1	-	59)	1	1	1	0	0
29)	1	1	1	0	-	60)	1	1	1	0	1
30)	1	1	1	1	-	61)	1	1	1	1	0
31)	0	0	0	0	0	62)	1	1	1	1	1

FIGURE 4

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Bit position	1	2	3	4	5	6	7,8	9	10		11-16		17	18-28	29	30-61	62
PNBA	1	1	1	1	0	1	0	1	1		0		1	0	1	0	1

FIGURE 5

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Bit position	1	2	3	4	5	6	7,8	9	10	11-13	14	15-16	17	18-28	29	30-59	60	61	62
PNBA	0	1	0	0	0	1	0	0	0	0	1	0	0	0	1	0	1	0	0

FIGURE 8

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Bit position	1	2	3	4	5	6	7,8	9	10	11-13	14	15-16	17	18-28	29	30-59	60	61	62
PNBA	0	1	0	0	0	1	0	0	0	0	0	0	0	0	1	0	0	0	0

FIGURE 9

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Row i	Listed Prefix	Decimal Value of Prefix String	Prefix Length  S	Next Hop Info	PNBA bit Position b <sub>i</sub>
1	0	0	1	nh <sub>1</sub>	1
2	00	0	2	nh <sub>2</sub>	3
3	0010	2	4	nh <sub>3</sub>	17
4	01	1	2	nh <sub>4</sub>	4
5	010	2	3	nh <sub>5</sub>	9
6	011	3	3	nh <sub>6</sub>	10
7	1	1	1	nh <sub>7</sub>	2
8	11	3	2	nh <sub>8</sub>	6
9	1110	14	4	nh <sub>9</sub>	29
10	11111	31	5	nh <sub>10</sub>	62

FIGURE 6

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The diagram shows a table with three columns: 'Position', 'Next Hop Info', and 'Associated PNBA bit Position'. Callout 60 points to the first column, 62 to the second, and 64 to the third. Callout 70 points to the entire table structure.

Position	Next Hop Info	Associated PNBA bit Position
1	nh <sub>1</sub>	1
2	nh <sub>7</sub>	2
3	nh <sub>2</sub>	3
4	nh <sub>4</sub>	4
5	nh <sub>8</sub>	6
6	nh <sub>5</sub>	9
7	nh <sub>6</sub>	10
8	nh <sub>3</sub>	17
9	nh <sub>9</sub>	29
10	nh <sub>10</sub>	62

FIGURE 7

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44      73      46      75      48      77      79

Row <i>i</i>	Listed 44	Decimal Value of bit Prefix 73	Full Prefix Length 46	Prefix Length  S  75	Next Hop Info 48	PNBA bit Position <i>b<sub>i</sub></i> 77	PNBA bit Value 79
1	0	0	1	1	nh <sub>1</sub>	1	1
2	00	0	2	2	nh <sub>2</sub>	3	1
3	0010	0	4	2	nh <sub>3</sub>	3	0
4	01	1	2	2	nh <sub>4</sub>	4	1
5	010	1	3	2	nh <sub>5</sub>	4	0
6	011	1	3	2	nh <sub>6</sub>	4	0
7	1	1	1	1	nh <sub>7</sub>	2	1
8	11	3	2	2	nh <sub>8</sub>	6	1
9	1110	3	4	2	nh <sub>9</sub>	6	0
10	1111	3	5	2	nh <sub>10</sub>	6	0

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FIGURE 10



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1)	0	-
2)	1	-
3)	0	0
4)	0	1
5)	1	0
6)	1	1

FIGURE 11



Bit Position	1	2	3	4	5	6
PNBA	1	1	1	1	0	1

FIGURE 12

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Diagram showing arrows pointing from labels 72, 85, 87, 89, and 91 to the corresponding columns of the table.

Position	Next Hop Info	First PNBA bit Position	Second PNBA bit Position	Third PNBA bit Position	Fourth PNBA bit Position	Fifth PNBA bit Position
1	nh <sub>1</sub>	1	-	-	-	-
2	nh <sub>7</sub>	2	-	-	-	-
3	nh <sub>2</sub>	3	-	-	-	-
4	nh <sub>4</sub>	4	-	-	-	-
5	nh <sub>8</sub>	6	-	-	-	-
6	nh <sub>3</sub>	-	5	-	-	-
7	nh <sub>5</sub>	-	-	1	-	-
8	nh <sub>6</sub>	-	-	2	-	-
9	nh <sub>9</sub>	-	-	-	5	-
10	nh <sub>10</sub>	-	-	-	-	2

FIGURE 13

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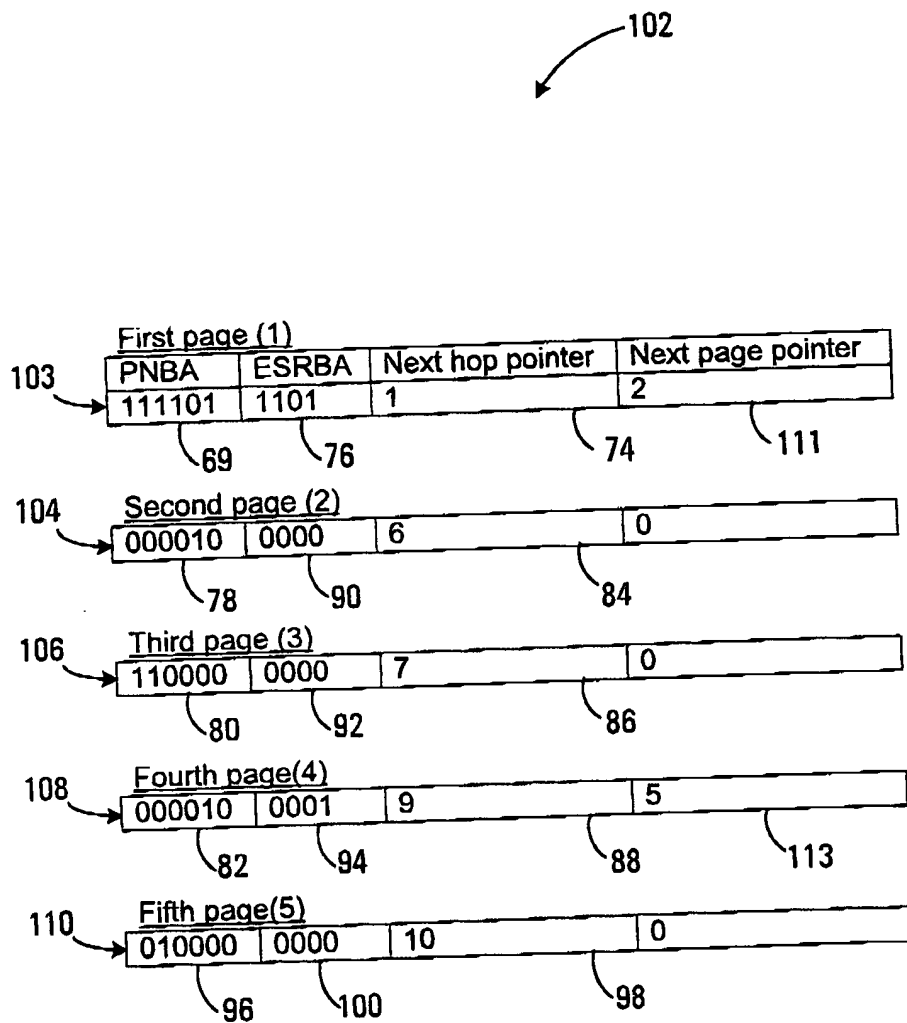


FIGURE 14

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FIRST ESRBA

Bit Position	1(00)	2(01)	3(10)	4(11)
ESRBA	1	1	0	1

FIGURE 15

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FIRST PNBA MASK

Bit position	1	2	3	4	5	6
PNBA	0	1	0	0	0	1

FIGURE 16

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SECOND PNBA MASK

Bit position	1	2	3	4	5	6
PNBA	0	1	0	0	1	0

FIGURE 17

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THIRD PNBA MASK

Bit position	1	2	3	4	5	6
PNBA	0	1	0	0	0	0

FIGURE 18

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FIRST RESULTANT PNBA

Bit position	1	2	3	4	5	6
PNBA	0	1	0	0	0	1

FIGURE 19

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FIRST ESRBA MASK

Bit position	1	2	3	4
ESRBA	0	0	0	1

FIGURE 20

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SECOND ESRBA MASK

Bit position	1	2	3	4
ESRBA	0	0	1	0

FIGURE 21

THIRD ESRBA MASK

Bit position	1	2	3	4
ESRBA	0	0	0	0

FIGURE 22

FIRST RESULTANT ESRBA

Bit position	1	2	3	4
ESRBA	0	0	0	1

FIGURE 23

SECOND RESULTANT PNBA

Bit position	1	2	3	4	5	6
PNBA	0	0	0	0	1	0

FIGURE 24

SECOND RESULTANT ESRBA

Bit position	1	2	3	4
ESRBA	0	0	0	0

FIGURE 25

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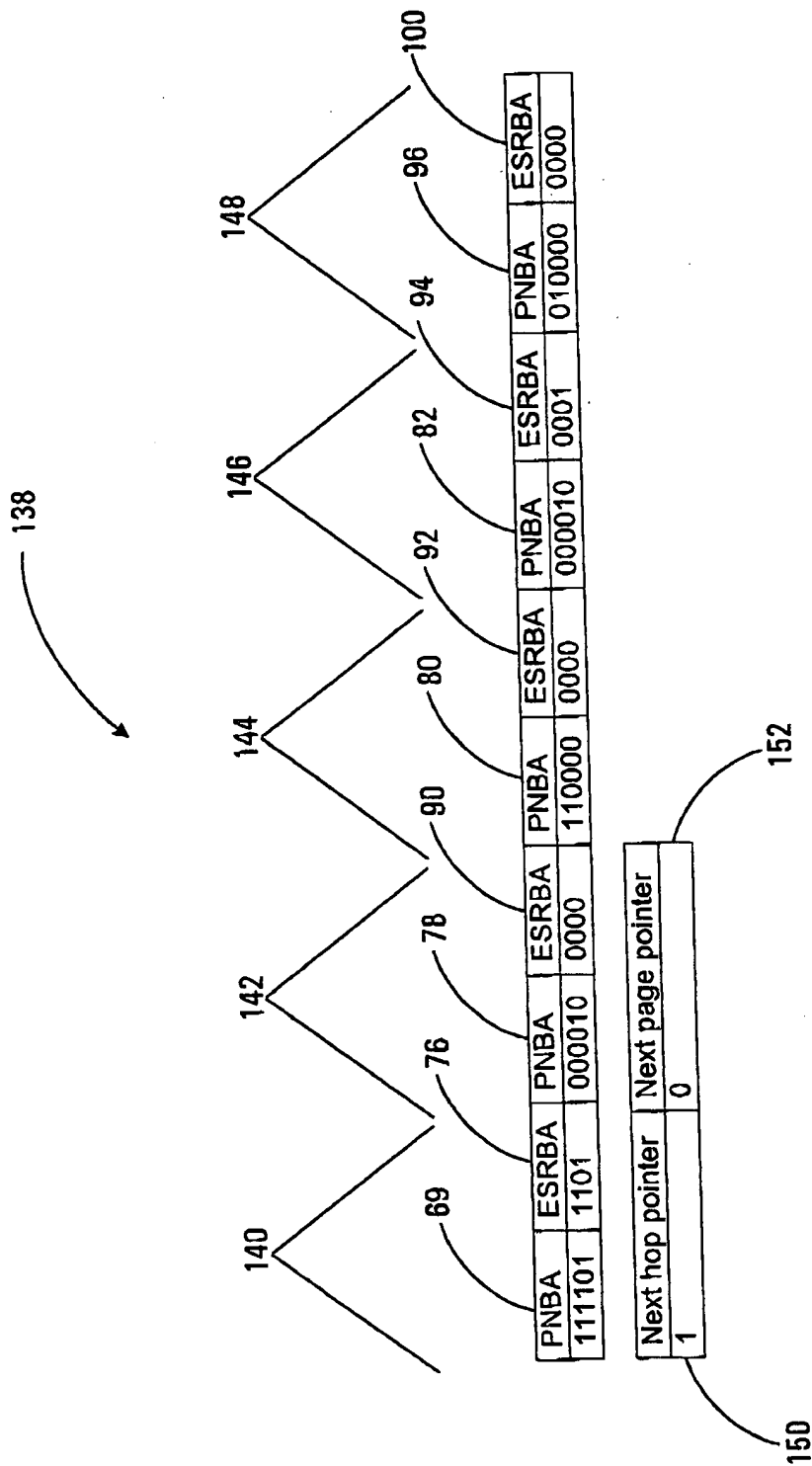


FIGURE 26